

Notice of References Cited	Application/Control No. 09/655,595	Applicant(s)/Patent Under Reexamination BEAUSOLEIL ET AL.	
	Examiner Thomas H. Stevens	Art Unit 2123	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,452,231	09-1995	Butts et al.	716/16
	B	US-5,963,735 A	10-1999	Sample et al.	703/24
	C	US-			
	D	US-			
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	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Donath-W.E., Placement and Average Interconnection Lengths of Computer Logic" 1979. IEEE pg.272-276
	V	IBM. "Machine Organization and Rent's Rule" 1999. pg.1-6
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.